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54 Diagnostic overlay and method of diagnosing electronic circuits.

57 In an overlay (23) for an electronic circuit used as a diagnostic apparatus, and in a method of conducting the diagnosis using the overlay, a plurality of temperature sensitive areas (25) which are made from encapsulated liquid crystal zones (27) are positioned on the overlay in accordance with the spatial configuration of the electronic components with which the overlay is to be used. Each of the temperature sensitive areas (25) is aligned with a respective component on the electronic circuit and each of the liquid crystal zones (27) has a first colour identical to the colour of the rest of the crystals when the respective electronic components are within a predetermined temperature range. When the temperature of a component changes by a certain quantity, the colour of the crystal zone also changes, which change is visually observed.

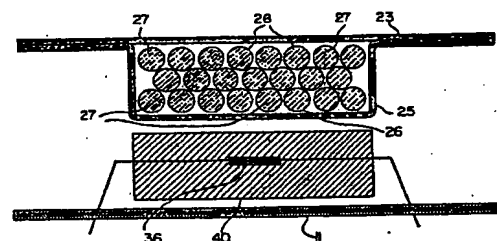


FIG. 3

EP 0 252 753 A1

Description

DIAGNOSTIC OVERLAY AND METHOD OF DIAGNOSING ELECTRONIC CIRCUITS

INTRODUCTION

This invention relates to a diagnostic device for electronic components and, more particularly, to a diagnostic overlay for an electronic circuit board which utilizes a plurality of encapsulated liquid crystal zones being colour indicative in respect of temperature changes.

BACKGROUND OF THE INVENTION

Electronic circuits comprise a plurality of operating electronic components, typically including transistors, resistors, delays and integrated circuits ("ICs") or chips. Such ICs may include ROMs, memory chips, drivers, receivers, gates and micro-processors. ICs serve a variety of tasks and may be classed on the basis of their complexity such as being small scale integration ("SSI"), medium scale integration ("MSI"), large scale integration ("LSI") or very large scale integration ("VLSI") type ICs depending on the relative complexity of the chip. The speed at which an IC processes signals may vary between direct current (D.C.) levels and frequencies of several megahertz and higher. The various electrical parameters such as voltage and current are fixed during manufacture and are generally described as a range in the manufacturers specifications by way of a defined minimum, maximum and typical. The waveforms processed by an IC may be classified as either digital or analog. Analog waveforms may take any shape while digital signals are ideally square waves. Various factors, including the functional integrity of the electronic component, may prevent the appearance of a true square wave. Each of the integrated circuits is required to operate normally in order to maintain the operating characteristics of the electronic circuit as a whole.

When the operation of a component on the board is deficient, the operating characteristics of the board change and if the change is of a sufficient magnitude, the operating characteristics will be deficient leading to a breakdown of the desired electronic response of the circuit as a whole.

Determining the source of electronic circuit problems has been difficult. Typically, the source is a deficient component but determining which component is at fault on the board has been a laborious task. Diagnostic tools previously used are sophisticated, difficult to operate, usually non-portable and expensive. In field repair situations, an emphasis may be on avoiding downtime and a field engineer is under pressure to get the system operational without delay. There is a strong tendency, therefore, to swap out boards or subsystems on a probability basis, leaving the task of actual fault identifications to the board repair facility. Upon arrival at a board repair depot, the boards are screened and the faults duplicated before any repair action is undertaken. This procedure can be time consuming, expensive, and cause fluctuations in inventory levels of spare boards which makes "just-in-time" management

principles difficult to implement.

The test technique of thermal image analysis is an industry accepted method of detecting electronic components with abnormal thermal characteristics. For proven circuit designs, such defects are generally indicative of IC failure or board artwork defects while in new designs the technique can be used to locate underspecified components or poorly design ventilation.

The prediction of the thermal activity of electronic components involves complex calculations involving numerous variables and parameters, many of which may not be easily measured or predicted themselves. While mathematical models have been constructed to describe electronic components in a laboratory environment under controlled conditions, no practical universal formula can be presented to predict the operating temperature of an IC under all circumstances. Generally, predictions regarding thermal activity must be derived from extrapolation of the data obtained by sampling good components operating under a variety of fixed conditions.

For a certain amount of D.C. power dissipated in a semiconductor, the junction temperature reaches a value determined by such factors as the thermal conductivity of the chip carrier materials and the differential temperature of the environment. As contained in the Motorola Application Note AN-509, the junction temperature may be calculated for a steady state condition by the following formula:

$$T(j) = P(d) \cdot R(\text{stdy}) + T(\text{amb})$$

Where: $T(j)$ = junction temperature

$P(d)$ = power dissipated at the junction

$R(\text{stdy})$ = steady state thermal resistance-junction to ambient

$T(\text{amb})$ = ambient temperature

The above equation holds true only for DC power at thermal equilibrium. Under dynamic conditions, the thermal response of the system must also be taken into account and duty cycle analysis must be performed. The junction temperature at the end of a pulse train will not equal the sum of the average temperature rise values because cooling occurs between pulses. Thus, the following equation is applicable:

$$T(\text{jav}) - T(c) = R(jc) \cdot P(d) \cdot D$$

Where: $T(\text{jav})$ = average junction temperature increase

$T(c)$ = chip case temperature

$R(jc)$ = thermal resistance-junction to case

$P(d)$ = DC power

D = duty cycle

To further complicate matters, the concept of duty cycle is based on the presence of a steady pulse train of identical square waves, a condition not often seen in practical electronic circuits. Most waveforms are somewhat non-rectangular having varying frequencies and periods.

Of the various damage mechanisms operating on electronic components, many have been found to be linked to power density fluctuations, either as cause

or effect. By virtue of the above equations, such damage mechanisms are ultimately tied to temperature variations. Thus, when the operation of an electronic component becomes deficient by any mechanism which alters the net level of power dissipation and such deficiency is of sufficient magnitude and duration as to alter the temperature of the outer surface of the component, then the temperature change recorded on the outer surface will yield a relative measurement of the deficiency. Further, the elevated temperature may itself constitute a damage mechanism causing secondary breakdown.

SUMMARY OF THE INVENTION

According to one aspect of the invention, there is provided a method of diagnosing an electronic circuit comprising positioning an overlay in substantial contact with said circuit, aligning at least one of a plurality of temperature responsive liquid encapsulated crystal zones on said overlay with a respective one of a plurality of electronic components on said circuit and observing the colour of said encapsulated crystal zones while said circuit is under operation for a predetermined time period.

According to a further aspect of the invention, there is provided an overlay operable to be positioned in substantial contact with at least one electronic component of an electronic circuit, said overlay comprising at least one encapsulated liquid crystal zone operable to be aligned with a respective component of said circuit, said liquid crystal zone being colour indicative to a predetermined temperature of said component.

According to yet a further aspect of the invention, there is provided an overlay for an electronic circuit, said overlay comprising a plurality of temperature responsive micro-encapsulated liquid crystal zones positioned in respective areas of said overlay, each of said liquid crystal zones having substantially the same colour at each of a plurality of operating temperatures, each of said crystal zones differing in colour when each of said plurality of operating temperatures changes by a predetermined quantity.

According to yet a further aspect of the invention, there is provided a microencapsulated liquid crystal zone operable to be brought into contact with at least one electronic component, said microencapsulated liquid crystal zone being colour indicative to a predetermined temperature of said component.

BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWINGS

A specific embodiment of the invention will now be described, by way of example only, with the use of drawings in which:

Figure 1 is a partial isometric view of a typical printed circuit board used on an IBM PC personal computer;

Figure 2 is a plan view of a diagnostic overlay according to the invention used with the printed circuit board of Figure 1;

Figure 3 is a diagrammatic cross-sectional enlarged view of a single integrated circuit on the circuit board with the overlay of Figure 2

illustrated in its operating position.

Figure 4 is a partial isometric and diagrammatic view of a further printed circuit board similar to Figure 1;

Figure 5 is a plan view of the plurality of diagnostic overlays used with the printed circuit board of Figure 4; and

Figure 6 is a partial isometric and diagrammatic view of the circuit board of Figure 4 being used with the overlays of Figure 5.

DESCRIPTION OF SPECIFIC EMBODIMENT

Reference is initially now made to Figure 1 where the mother printed circuit board from an IBM PC personal computer is generally illustrated at 10. It consists of the board 11 itself, and a variety of components such as integrated circuits including ROMs 12, RAM chips 13, and gates such as that gate at 21. The various components extend outwardly from the printed circuit board 11 a variety of distances depending on the design of the circuit as a whole and the various components as clearly observed in Figure 1.

The diagnostic overlay is shown generally at 23 in Figure 2. The overlay 23 is made of transparent mylar and it carries a plurality of temperature sensitive zones 25 which are each made up a plurality of microcapsules 26, each capsule carrying a number of liquid crystals 27 which are color temperature sensitive. The microencapsulated liquid crystals are illustrated more clearly in Figure 3. The micro-encapsulated liquid crystals 26 are bonded within the temperature sensitive zones 25 on the overlay 23 and each zone 25 corresponds in location to the heat source zone which is typically the central area of the corresponding integrated circuit or electronic component on the circuit board 11. For example, Figure 2 illustrates the indexing rectangles surrounding the temperature sensitive zones for integrated circuits of five different sizes. The size of each rectangle corresponds to the size of each component with which contact is desired and the location of the rectangle is identical to the location of the component on the printed circuit board 11. For example, the temperature sensitive zone 24 corresponds to integrated circuit 30 on the board 11; the temperature sensitive zone 35 corresponds to integrated circuit 32 and the temperature sensitive zone 33 corresponds to the integrated circuit 34. The color of each temperature sensitive zone on the overlay 23 is designed to be the same under normal operation depending on the normal operating temperature of each corresponding integrated circuit on the printed circuit board 11.

Figure 3 illustrates the printed circuit board 11 with a typical component comprising the integrated circuit 36 and the carrier 40 illustrated as extending outwardly therefrom. The overlay 23 diagrammatically illustrates the temperature sensitive zone 25 within which are positioned the microcapsules 26 carrying the liquid crystals 27. The temperature sensitive zone 25 is illustrated just prior to contact with the integrated circuit 36.

The various components which are positioned on the board 11 may extend outwardly from the board

11 various distances. To allow contact between each series of components having the same height and the overlay 23, the overlay 23 is divided into section. Two such sections 45, 46 are illustrated in Figure 2. These sections and others may be removed from the overlay 23 by way of perforations, etc. so that they may be positioned on the components or the circuit board 11 to which they relate, such components having the same height.

Referring now to Figure 5, the diagnostic overlay 23 is not only divided into different sections but there is provided a further temperature responsive liquid crystal encapsulated zone 50 on the overlay 23 which is not associated with an electronic component on the printed circuit board 14 of Figure 4. This further zone 50 may be printed on each of a plurality of overlays 23, each overlay 23 being designed for the same printed circuit board but each overlay 23 being for use at different ambient temperatures. The further zone 50, when appropriate, gives a visual indication of when the overlay 23 may be used in accordance with the particular ambient temperature. The visual indication may be, for example, a colour such as green or a letter readout such as "USE ME" when the ambient temperature is appropriate for that particular overlay.

A colour standard chart 51 may also be used on the overlay 23 as seen in Figure 5. The colour standard chart 51 is simply a comparator for comparing the colour of the liquid crystal zone on top of an electronic component. This may be useful, for example, when the overlay 23 is viewed under different lighting conditions such as outdoor or natural lighting conditions and indoor or artificial lighting conditions. The chart 51 is not temperature sensitive and the colours given in the chart 51 are arranged in the same order as the colours in the liquid crystal change when the temperature of the particular electronic component changes.

OPERATION

In operation, there may be provided a plurality of overlays 23, each being identically configured for each circuit board 11. As earlier explained, each overlay 23, however, is constructed for use in a different range of ambient temperatures. For example, if the board 11 was to be operated under varying temperature conditions, then a series of overlays 23 would be constructed to be correctly colour indicative under the various ranges of these conditions. To this end the further liquid crystal encapsulated zone 50 (Figure 5) will be consulted to ensure that the particular overlay 23 is the correct one to be used under the particular ambient temperature conditions. An indicating signal such as a green colour or a visual message will appear on the zone 50, the presence of such colour or visual message indicating to the user that the particular overlay 23 is the correct one to be used.

After the correct overlay 23 has been chosen, each temperature sensitive zone 25 on the overlay 23 is located opposite its corresponding integrated circuit, the indexing rectangles of Figure 2, for example, surrounding the temperature sensitive zones being on a 1:1 relationship with its integrated

circuit. The overlay 23 is then brought into contact with the circuit board 10. The 1:1 relationship between the rectangles surrounding the temperature sensitive zones and the outermost surface of each integrated circuit is for easy alignment or indexing purposes.

Where there are a series of components which have a different height on the circuit board 14 than the remaining components such as those illustrated in Figure 4 and 6, the indexing rectangles together with their temperature sensitive zones may be removed from the overlay 23 by using perforations or by cutting as illustrated in Figure 6. These portions are then aligned with their own respective components.

The circuit is then subject to the correct operating voltage and the colour of the temperature sensitive zone is observed after a certain time period depending on the ambient temperature. For example, it is known that the proper operating temperature of the ROM chips 12 on the printed circuit board 11 is approximately between 24°C and 26°C with about 24°C being optimum after one minute of operation at 20°C ambient temperature. The corresponding liquid crystal is designed to appear green within this operating temperature range. Likewise, it is known that the operating temperature of the RAM chips 13 is between 23.5°C and 24.5°C under the same conditions. Again, the corresponding liquid crystal is designed to appear green when the chip is operating at the correct temperature. Thus, an observer would see a plurality of crystals all appearing green when the printed circuit board is operating correctly after the specified time lapse.

Where, however, a component is not operating correctly; that is, when there is a deficient integrated circuit, it will, in a great majority of cases, increase in temperature beyond its normal operating temperature. In this event, the temperature sensitive zone will not be seen as green but, typically, its colour will range higher on the colour spectrum to, say, blue or violet. Outside the visible range, the liquid crystal will appear black. If the temperature, as may be the case in certain defective operating characteristics, is lower than the correct operating temperature of the component, the colour of the liquid crystal will typically be lower on the visible spectrum to, say, red or brown. Again, black may also be used at the lower range. Where the component is defective, therefore, the colour of the liquid crystal is readily observed to differ from the green colour showing normal operation and such a component can be quickly identified.

The components of an electronic circuit are generally highly interactive and, as such, the presence of a fault in one component will often alter the power level and, hence, the operating temperature of a second component with which it interacts. In this case, the faulty component will often become visible first, followed by a colour change at the second component at a later time. As a general rule, the first found faulty element should be considered the most serious. The component will then be replaced and the circuit will be retested.

Under different lighting conditions, the colours of the encapsulated liquid crystal zones 25 may appear different. To aid in determining the correct colour, the user may refer to the colour standard chart 51 (Figure 5). The colour standard chart 51 is not, of course, temperature sensitive and the colours in the chart 51 are arranged in the same order as the colour change in the particular liquid crystal zone 25 will proceed as the temperature changes.

A number of modifications are currently contemplated in the embodiment specifically described. For example, although the colored rectangles surrounding the temperature sensitive zones are required only for properly indexing the temperature sensitive zones with the corresponding electronic components, they are otherwise not necessary. Any number of indexing devices may be utilized for proper alignment including pins extending between the overlay and the circuit board, rectangular openings for other components and the like.

While the overlay illustrated has been described as being made from transparent mylar, such a configuration is again used for alignment purposes only together with the rectangles. Assuming indexing is performed using other techniques, the overlay could be opaque or colored as desired.

The fact that the temperature reading should be taken at a specific time after the correct operating voltage is applied to the board at a certain ambient temperature need not be limiting. For example, a temperature-time graph could be provided which would give the correct reading time for each ambient temperature. Thus, the overlay can be used over a wide range of ambient temperatures. In addition, a thermometer only may be mounted on the overlay 23 to ensure that the ambient temperature is correct when the colour changes are to be observed.

While the liquid crystal zones 25 have been described as being generally the shape of the component which is being tested, the zones 25 may clearly be only a small portion such as a circle 52 of the area of the various indexing rectangles 53 as illustrated in Figure 5.

Similarly, while the overlay 23 has been described as a device separate from the printed circuit and the electronic components which make up the printed, it is contemplated that the overlay 23 may be permanents affixed to the circuit or, alternatively, that a plurality of individual liquid crystal encapsulated zones could be used, one of each of the zones being permanents affixed to a particular electronic component which would provide a built in or permanent thermal imaging system thus eliminating the need for alignment.

Many other changes may be made in the apparatus described and the specific embodiment set out herein should be taken as illustrative only and not as limiting the scope of the invention as defined in accordance with the accompanying claims.

Claims

1. A method of diagnosing an electronic

circuit comprising positioning an overlay substantially in contact with said circuit, aligning at least one of a plurality of temperature responsive liquid encapsulated crystal zones on said overlay with at least a respective one of a plurality of electronic components on said circuit and observing the colour of said encapsulated crystal zones while said circuit is under operation for a predetermined time period.

2. A method as claimed in claim 1 characterised in that said overlay is transparent.

3. A method as claimed in claim 2 characterised in that said overlay is mylar.

4. A method as claimed in any preceding claim and further characterised by indexing the position of said overlay relative to said circuit board.

5. A method as claimed in any preceding claim and further characterised by selecting the appropriate one of a plurality of overlays operable to be used with the same electronic circuit by referring to a further temperature responsive liquid encapsulated zone, said further zone being on each of said plurality of overlays.

6. A method as claimed in any preceding claim and further characterised by comparing said colour of said zone or zones being observed with a series of colour standards appearing on said overlay.

7. An overlay operable to be positioned in substantial contact with at least one electronic component of an electronic circuit, said overlay comprising at least one encapsulated liquid crystal zone operable to be aligned with a respective component of said circuit, said liquid crystal zone being colour indicative to a predetermined temperature of said component.

8. An overlay as claimed in claim 7 wherein said overlay comprises a plurality of said encapsulated liquid crystal zones, each of said encapsulated crystal zones being visually colour responsive to a predetermined temperature of a respective component of said circuit.

9. An overlay as claimed in claim 8 wherein each of said encapsulated liquid crystal zones is substantially the same colour at a specified time when each of said crystal zones is at a first predetermined temperature.

10. An overlay as claimed in claim 8 or 9, wherein each of said encapsulated liquid crystal zones has a visually observable colour change when said first predetermined temperature changes by a predetermined value.

11. An overlay as claimed in any of claims 7 to 10 and further comprising alignment indexing means on said overlay.

12. An overlay as claimed in any of claims 7 to 11 wherein said overlay is transparent.

13. An overlay as claimed in claim 12 wherein said overlay is mylar.

14. An overlay as claimed in any of claims 7 to 13 wherein said overlay is affixed to said electronic component.

15. An overlay as claimed in any of claims 7 to

14 comprising a further temperature responsive encapsulated liquid crystal zone, said further zone being colour responsive to ambient temperature changes.

16. An overlay as claimed in any of claims 7 to 15 and further comprising a series of colour standards on said overlay.

17. An overlay for an electronic circuit, said overlay comprising a plurality of temperature responsive micro-encapsulated liquid crystal zones positioned in respective areas of said overlay, each of said liquid crystal zones having substantially the same colour at each of a plurality of operating temperatures, each of said crystal zones differing in colour when each of said plurality of operating temperatures changes by a predetermined amount.

18. An overlay as claimed in claim 17 and comprising a further temperature responsive liquid crystal zone, said further zone being colour responsive to ambient temperature changes

19. An overlay as claimed in claim 17 or 18 and further comprising a series of colour standards on said overlay.

20. A microencapsulated liquid crystal zone operable to be brought into contact with at least one electronic component, said microencapsulated liquid crystal zone being colour indicative to a predetermined temperature of said component.

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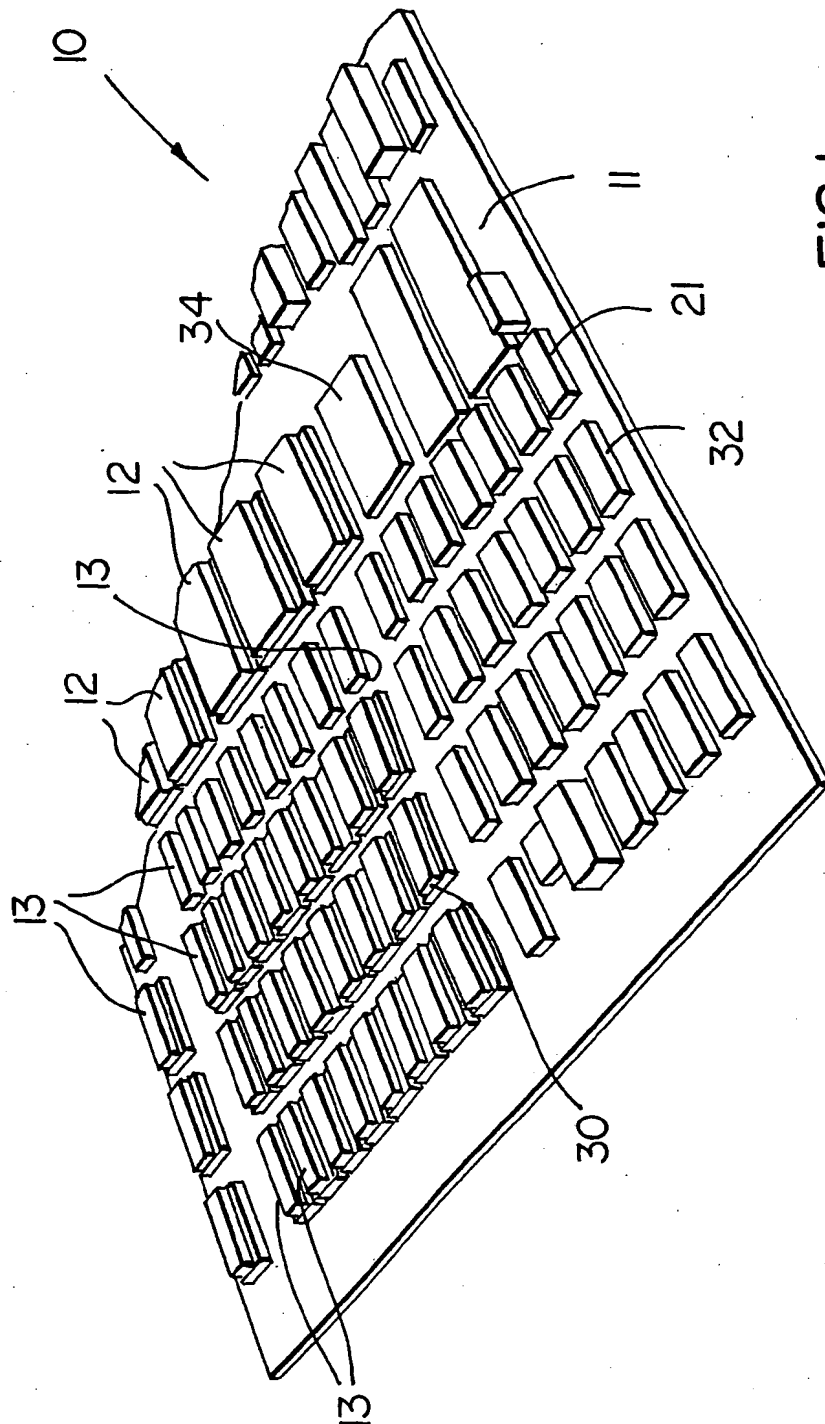
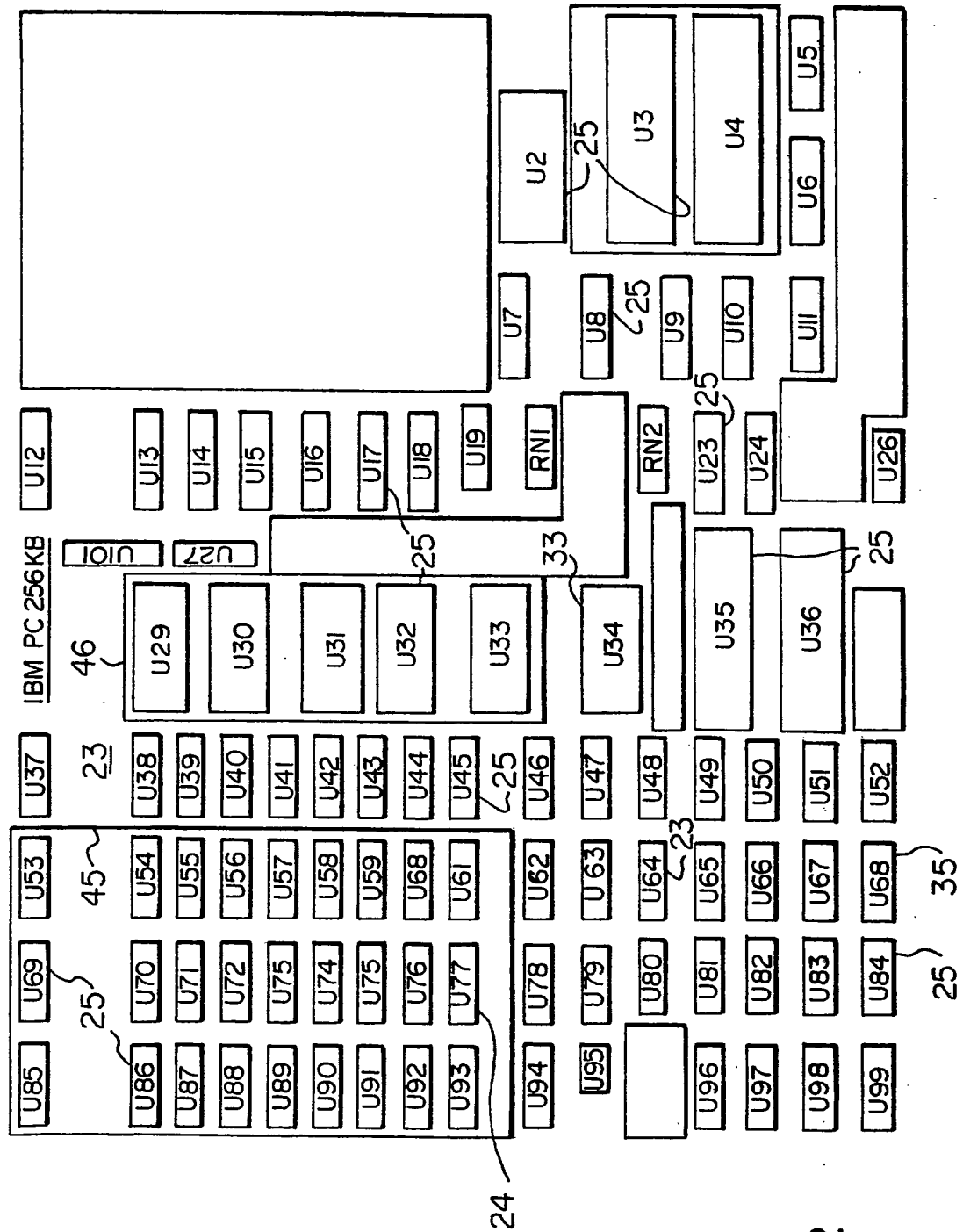


FIG. 1



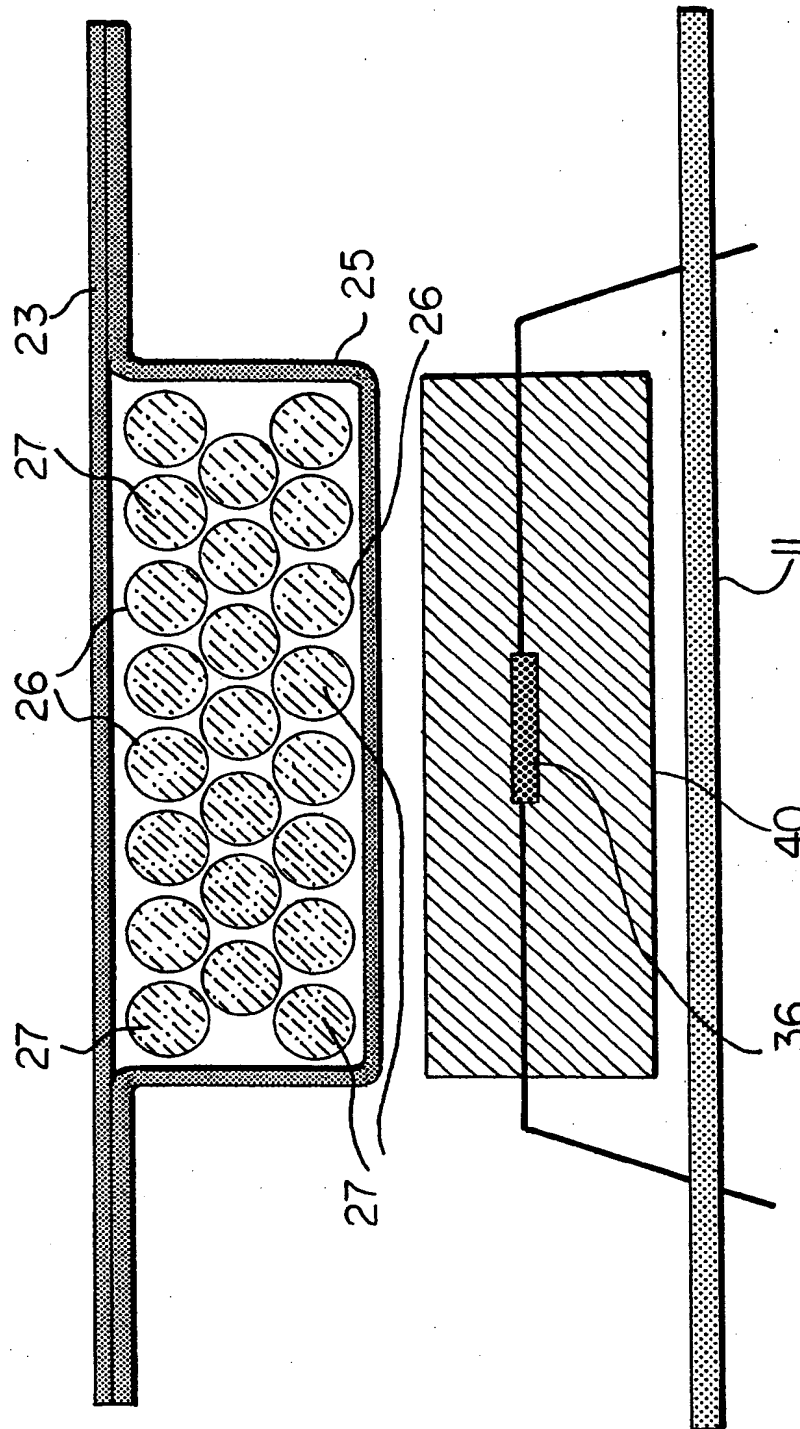


FIG. 3

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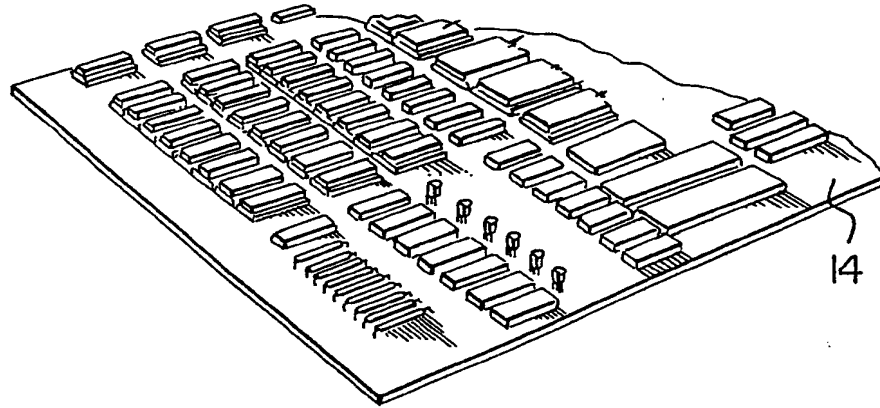


FIG. 4

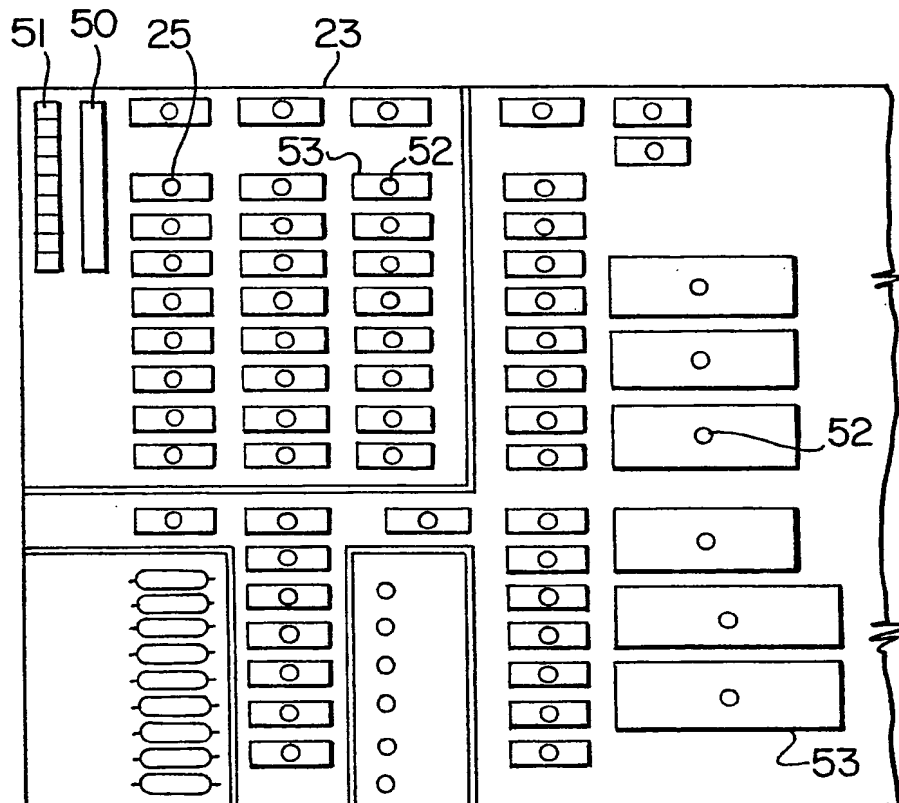


FIG. 5

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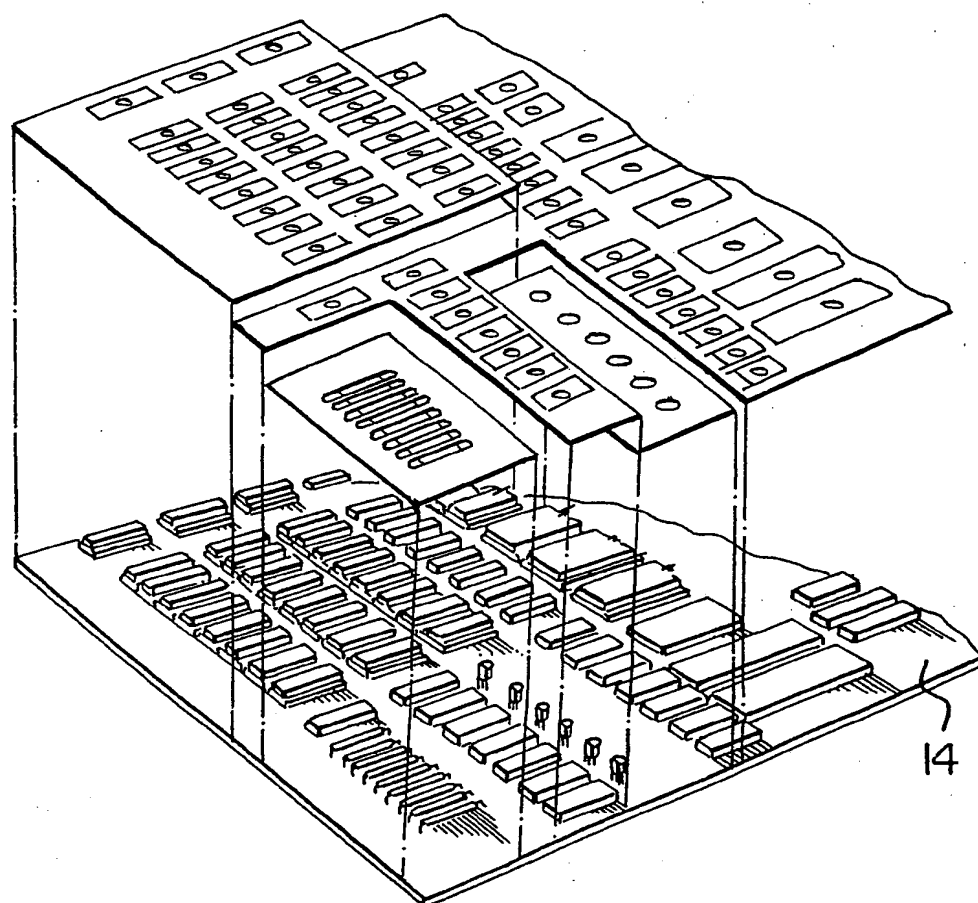


FIG. 6



European Patent
Office

EUROPEAN SEARCH REPORT

Application number

EP 87 30 6115

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 4)
X	US-A-4 392 102 (R.K. SUGALSKI et al.) * column 4, line 41 - column 6, line 8; figures 4-6 *	1-3, 7-14, 20	G 01 R 31/28 G 01 K 11/16
A		4-6	
A	--- EP-A-0 143 550 (BIOSYNERGY INC.) * claim 26 *	1-2	
A	--- US-A-3 889 053 (R.A. LLOYD et al.) * abstract; figure 1 *		

The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int. Cl. 4)
			G 01 K 11/16 G 01 R 31/28
Place of search BERLIN		Date of completion of the search 02-10-1987	Examiner LEMMERICH J
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

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